Paul Kinzelman

7 Parklane Circle, Peralta NM 87042
Home: (505) 865-4648
Cell: (505) 238-9988
Email: paul@kinzelman.com

Summary

Experienced Software engineer and pilot. Over 25 years engineering experience in the computer industry and 40 years in aviation. High degree of knowledge in all areas of computer hardware and software architecture. High degree of knowledge in general aviation and significant knowledge in commercial aviation.

Engineering Highlights	Aviation Highlights
Significant expertise in the following:	Over 11000 hours of flight time:

Engineering Highlights

Significant expertise in the following:
- Operating system driver code logic
- PDA application software
- Software simulation
- CAD tools and microcode design
- Quality Assurance

Holder of two US Software Patents.

High degree of knowledge in project and people management and problem solving.

Aviation Highlights

Over 11000 hours of flight time:
- 5000+ hours of multiengine
- 3500+ hours of turbojet experience
- 1900+ hours in Learjet Model 35A type
- 15+ years of Part-135 and -121 experience
- 7+ years Part-135 PIC experience
- Type ratings: LR-JET and CL-65 (CRJ)
- Ground instructor at Medflight
- Created safety seminar for medics

Aviation Employment History:

- <u>FAR part-135</u> Lear-35 *Medflight Air Ambulance*, ABQ, NM 1/2008-7/2015 and 2/2022-12/2025
  - Chief Pilot from 1/2023
  - Medical transport, on call 24/7.
  - Ground instructor for new pilots
  - Created the medical crew aircraft safety training seminar.
  - Created and wrote the pilot scheduling policy.

- <u>FAR part-135 Captain - C208 Caravan Fedex Feeder for *Empire Airlines*</u>, 10/2017 to 2/2022

- <u>FAR part-121 First Officer - CRJ Regional Jet - *SkyWest Airlines*</u>, 7/2015 to 8/2017

- FAR part-135 <u>Captain</u> - Cessna 402 - *South Aero*, Albuquerque, NM 4/2005-1/2008
  - Flew cargo from Albuquerque to points in New Mexico and Colorado.

Aviation Certifications

- FAA Airline Transport Pilot (ATP): Airplane multi-engine land

- FAA First Class Medical

- Commercial privileges: Airplane single engine land and sea, helicopter, and glider

- Certified Flight Instructor: Airplane single and multi-engine, helicopter, glider, and airplane instrument

- Type ratings: LR-JET and CL-65 (CRJ)

Computer Engineering History:

- Created and wrote a Lear Jet performance tool for the Android, webOS, and Windows.
  - See http://www.etold.us

- <u>UNIX Kernel Developer</u> Entercept Security Technologies 1/2001 - 2/2002 (now McAfee)
  - Supported and enhanced Solaris and Linux Intrusion Prevention software.

- Staff engineer   ISD Corp, 3/1999 - 1/2001 (now ESG)
    - responsible for sizing and working on contracts for other companies, including a Linux SCSI tape driver, embedded system download tool, and a Linux, HP-UX, and Solaris performance tool.

- Design Engineer   Tandem Computers, 1993 - 3/1999 (now HP)
    - Designed, implemented, and tested the ServerNet driver for NSK (Tandem's proprietary operating system) and for Windows/NT.
    - Created the first kernel-level tracing, fault injection, and hot upgrade facilities for effective debugging.
    - Created software test bed to more effectively debug the driver software.
    - Served as a mentor and technical director to other software and quality assurance engineers.
    - Contributed significantly to the design, debug, and quality assurance processes.

- Consulting Engineer Digital Equipment Corporation (DEC) 1974 - 1993 (now HP)
    - Lead responsible for the functional verification of numerous revenue-critical projects within DEC.
    - Provided CAD and Logic Simulation consulting to development groups within DEC.
    - Provided mentoring and technical direction to design and quality assurance engineers.
    - Pioneer in the design of random stimulus generation and simulation self-checking for complex systems. Previously, these techniques were applied exclusively at the processor chip level because of the complexities of checking system-level simulation results.
    - Created the concept of microcode-driven transactors (test benches). Created multi-threaded, precisely timed transactors driven by microcode allowing hardware engineers to verify individual components of a system.

- US Patent Holder
    - Patent no. 5594741 "Method for Control of Random Test Vector Generation"
    - Patent no. 6144930 "Method for Providing a Memory Model of a Memory Device for Use in Simulation"

- Technical Reviewer for the following publications:
    - *Understanding the Linux Kernel*, Daniel P. Bovet & Marco Cesati, O'Reilly Publications
    - *Linux Device Drivers*, Alessandro Rubini & Jonathan Corbet, O'Reilly Publications
    - *Exploring Expect*, Don Libes, O'Reilly Publications

- Authored Technical Articles for *VLSI Systems Design Magazine*:
    - *Transactors and Demons*, December 1988
    - *Behavioral Exercisers*, June 1990

- Lectured on the Principles of my work involving random stimulus generation and simulation self-checking for complex systems throughout DEC as well as several internal DECSIM symposiums. Traveled to Germany and Australia to present my simulation/verification courses to engineers

- Designed and Presented Courses
    - *Introduction to DECSIM* (Digital internal simulator)
    - *Advanced DECSIM* (Digital internal simulator)
    - *Introduction to Computer Aided Design* (CAD) Co-developed these courses designed for management and sales force to familiarize students with the concepts and advantages of CAD.
    My courses consistently received excellent ratings by students.

Education

- Bachelor of Science in Electrical Engineering, (BSEE), Carnegie-Mellon University 1974

Executive Positions Held

- DCU Credit Union Board of Directors, member from 1992 - 1996. DCU is the largest credit union in the Northeast, with $600M in assets in 1996.